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1374.43797X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: H. KIKUCHI, et al.

Serial No: 10/500,931

Filed: July 8, 2004

Title: FABRICATION METHOD OF SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICE

Group: 2812

Examiner: Stanetta D. ISAAC

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

June 27, 2006

Sir:

In response to the Office Action mailed March 27, 2006, please amend
the above-identified application as listed below, and as set forth on the
following pages:

Amendments to the Claims; and

Remarks are included following the amendments.